



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Zahi Said Abuhamdeh et al.

SERIAL NO.: 10/647,018

GROUP ART UNIT:

FILED: August 22, 2003

EXAMINER:

FOR: Microprocessor Based
Self-Diagnostic Port

ATT'Y DOCKET: TRA-078

Honorable Commissioner of Patents
and Trademarks
Washington, D.C. 20231

Sir:

I hereby certify that this correspondence is being deposited
with the United States Postal Service as first class mail in
an envelope addressed to: Commissioner of Patents and
Trademarks, Washington, D.C. 20231 on JAN 13, 2004
David P. Gordon
David P. Gordon
Reg No. 29,996
Date JAN 13, 2004

PRELIMINARY AMENDMENT

Kindly amend the application as follows.

In the Specification:

Page 1, line 2, insert the following paragraph.

This application claims the benefit of provisional
application Serial Number 60/445,407, filed February 6, 2003,
entitled "Novel JTAG Master for Support of Chip Level Self-
Diagnostics", the complete disclosure of which is hereby
incorporated by reference herein.